**BADAL KUMAR PANDA**

**SILICON VALIDATION ENGINEER-1**

**Professional Summary: ￼**

An innovative and highly motivated Electrical Engineer with **6 months** of industry standard training experience and currently full-time experience in bench testing. implementing NI LabVIEW & Python Programming, GPIO Testing, Validation & Verification.

**SKILLS:**

* Handling instruments from other non-NI hardware through **LabVIEW** and **Python** Programming.
* Hands-on experience in all the Lab Test instruments like **Power supply, DMM, Source Measure Unit, Function generator** and **Scope**.
* Could be able to write the Automation test program using the Instruments **SCPI** commands & **LabVIEW** for Instrument Control and testing.
* Hands-on Experience in creating test setups and executing test plan for **GPIO Chip Validation**.
* Hands-on experience with **JTAG Protocol** for Chip testing and Debugging.
* Protocols used: **Serial Test Access -JTAG, GPIB, USB**.
* Good Knowledge of documentation from the requirement phase to planning, design, implementing, testing and closure of the project.
* Adopt a creative approach to problem-solving through good analytical and technical skills and be flexible in handling multitasking effectively.
* Ability to learn and master new technologies to deliver outputs in a short period.

**PROJECTS HANDLED**

**Project Name: Synopsys IP Char**

**Role: Test Engineer**

**Description:**

* validated the GPIO, ODIO, LVDS, I3C Modules of Test chips across PVT Conditions, created test setups based on test plans, and collected measurement data.
* Performed the PVT Test for DC test cases like Schmitt, Pad Leakage, IOL/IOH, Standby, I/O OFF, Core OFF, Weak Pull up, weak Pull down, VOL/VOH, Active Current. And, for Ac test case such as TX Timing, RX Timing.
* Executed tests under multiple temperature conditions (-40C, 125C) using Themestream For reliable validation across corners. And for Typical device (25C, -40C, 125C) Temperature used.
* Automated the Portions of test flow using Python and LABVIEW.
* To Access the Control pin, we use NI Pxie 6750 Digital Pattern Generator. And Created different Pattern for Different test case’s According to the Test Plan.
* Instruments used for Automation POWER SUPPLY, SMU, DMM, THERMOSTREAM, FUNCTION GENERATOR, DSA.

**PERSONAL PROFILE: ￼**

Father’s Name: SANTOSH KUMAR PANDA

Gender: MALE

Date of Birth: 09-02-2004

Languages known: English, Hindi, Odia

Address:Phulbani, Kandhamal, Odisha, 762001.

**ACADEMIC CREDENTIALS:**

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| --- | --- | --- | --- | --- |
| **Qualification** | **Board/University** | **School/college** | **Year** | **Percentage** |
| B.E (EEE) | BPUT, Odisha | Parala Maharaja Engineering College, Berhampur, Odisha | 2025 | 82% |
| CHSC | Odisha State Board | Govt Higher Secondary Education, Phulbani, Kandhamal, Odisha | 2021 | 73.83% |
| HSC | Odisha State  Board | Saraswati Sishu Vidya Mandir, Phulbani, Kandhamal, Odisha | 2019 | 71.83% |